Organizing Committee

PATRON:

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- Dr. K.C Patra, Director, C.V. Raman Group of Institutions, Bhubaneswar

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- Mr. Sanjay Mohapatra, Hon. Secretary CSI
- Mr. Manas Pattnaik, Chairman CSI Bhubaneswar Chapter

CONVENER

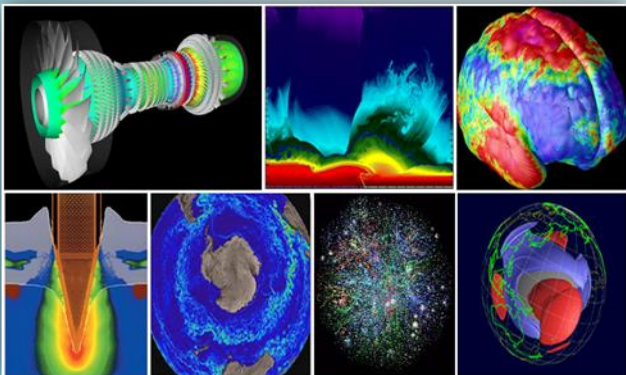- Dr. R. Misra, HOD, Dept. of IT, CVRCE

CO- CONVENER

- Dr. B. Pati, HOD, Dept. of CSE, CVRCE

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Prof. A. Das	Prof. M. R. Panda
Prof. D. Mohanty	Prof. S. K. Bisoyi
Prof. N. Dash	Prof. S. C. Nayak
Prof. R. Priyadarshini	Prof. S. R. Sahoo
Prof. B. Panda	Prof. D. Panda
Prof. A. K. Sahoo	

Workshop on Parallel Heterogeneous Computing

Registration Form

Name:(In Block Letters): _____

Institute/Organization: _____

Designation: _____

Address for Correspondence:

Email and Mobile No:_____

Accommodation: (Please Tick)

Student Hostel / Hotel / Not required

Food Habit (Please Tick): Veg / Non-Veg

Payment Details:

DD Number:
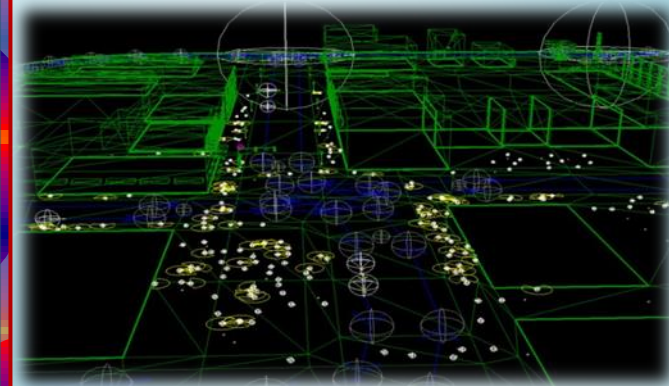
Bank Name:

Amount::

Date:

Signature:

TECHNICAL COLLABORATORS

NATIONAL WORKSHOP

On

PARALLEL & HETEROGENEOUS COMPUTING

AUG 10-14, 2015

ORGANIZED BY

NVIDIA

GPU Education Center
C.V. Raman College of Engineering,
Bidyanagar, Mahura, Janla,
Bhubaneswar-752054

About C.V. Raman College of Engineering

C.V. Raman College of Engineering Bhubaneswar was established in the year of 1997 in the name of Nobel laureate Sir C.V.Raman with an objective to impart quality education and training leading to BTech, MTech, MBA and MCA. This college is an ISO 9001:2008 certified, NBA, NAAC accredited technical institution approved by AICTE and affiliated to BPUT Odisha. It is situated in lush green environment on NH-5. It is 10 kms from Barmunda Bus stand, 17 kms from BBSR Railway Station & 15 kms from Biju Patnaik International Airport towards khurda.

Outline and Objective of the Workshop

Despite the extraordinary advances in computing technology, we continue to need greater and greater computing power. High Performance Computing (HPC) is becoming an important underpinning of many domains of research. The need for best   computing power can be met through the use of parallel computers. Recent years has seen use of parallel systems with trends towards heterogeneous systems consisting of general-purpose cores and acceleration devices. Heterogeneous computing offers the potential to move to multiple devices and computing units collaborating for required computation.

This Workshop is designed as a combination of invited lectures from Academia and Industry, contributed research paper presentations on the different aspects of Parallel and Heterogeneous computing. It will also include a workshop on parallel programming using CUDA and student competitions.

Registration :

Registration by 31st July

- Students   Rs. 300.00
- M.Tech/   Rs. 600.00
  Research Fellow/
  Faculty

(Registration fee does not include accommodation)

Payment Details

Please send registration fee by draft in favour of **(C V RAMAN COLLEGE BHUBANESWAR-STUDENT BR.-C S I)** *or by cash and filled in registration form to –*
  Dr. Rachita Misra**.** Head, Dept. of I.T,
  C.V. Raman College of Engineering,
  Bidyanagar, Janla, Mahura,
  Bhubaneswar-752054, Odisha, India

Contacts for More Information

- **Mr. A.K.Sahoo**, Assistant Professor, Dept of Information Technology,(91-9861443456)
- **Dr. R. Misra**, Professor and Head, Dept of Information Technology, (91-94384 85177)
- **Dr. B. Pati**, Professor and Head, Dept.of Computer Science & Engg. (91-8895033020)

Mail: *nwphc2015@gmail.com*

Tentative Speakers

- **Prof. Sudip Misra, IIT KGP**
- **Prof. P. S. V. Nataraj, IIT Bombay**
- **Prof. S. Gopalakrishnan, IIT Mumbai**
- **Prof. P.M. Khilar, NIT RKL**
- **Prof. P.K.Jena, ISM Dhanbad**
- **Dr. Diptendu Sinha Roy, NIST**
- **Prof. S.N. Dehuri, FM University**
- **Mr. Hari Shankar, Infosys**
- **Mr. Saroj Kumar Choudhury, TCS**
- **Mr. Vinay K. Deshpandey, NVIDIA**.

Program Schedule

- ➢ **Invited Lectures**
- ➢ **Industry Speakers**
- ➢ **R&D Paper Presentation**
- ➢ **NVIDIA CUDA workshop**
- ➢ **Student Competitions**

Researchers and Faculty members are requested to send R&D papers in IEEE format maximum 6 Pages to : *nwphc2015@gmail.com*. The topics for contributed papers are:

- ➢ **cloud computing,**
- ➢ **big data analytics,**
- ➢ **GPU computing,**
- ➢ **Machine intelligence,**
- ➢ **Wireless Sensor Network,**
- ➢ **Internet of things,**
- ➢ **Human computer interface.**
- ➢ **Other similar areas**

R&D papers must be original innovative work of the authors not published in any journal or conference. All papers will be peer reviewed and published in an international journal.

SPONSORSHIP CERTIFICATE

Certified that Mr./Ms./Dr........................... is an employee of our institute and is hereby sponsored for the National Workshop on " Parallel and Heterogeneous Computing", at C. V. Raman College of Engineering, Bhubaneswar during the period 10th to 14th August 2015.

He/She will be permitted to attend the course, if selected.

It is also certified that our institute is an AICTE approved institute.

Place: Name & Signature

Date:

(Director/Principal)

(Seal of the institution)

IMPORTANT INFORMATION

Selection will be on first come first serve basis subject to AICTE and CVRCE norms.

Last date for receipt of applications: 30-07-2015.

Intimation of selection:

on or before 30-07-2015.

ADDRESS FOR CORRESPONDENCE

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CVRCE, Bidyanagar, Mahura, Janla,

Bhubaneswar– 752054,

Mail: *nwphc2015@gmail.com*