

C. V. RAMAN COLLEGE OF ENGINEERING, BHUBANESWAR

CVRCE/EXAM/416/15

Date:- 19th November '15


NOTICE for CLASS TEST – II of 1st Sem M.Tech (15) Admission Batch

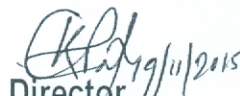
It is hereby notified for the information of the students of M.Tech course that the Class Test – II of 1st Sem M.Tech for the students of 15 Admission batch will be held on **26th, 27th & 28th Nov.'15** from **10:00 – 11:00AM & 11:30AM – 12:30PM** as per the following schedules. The concerned faculties are requested to send the question paper of their subjects to Exam Cell in a sealed cover on or before **25.11.15** positively.

Date	Branch	1 st Sitting	2 nd Sitting
		10:00 – 11:00AM	11:30AM – 12:30PM
26.11.15	ECE	Modern Digital Commn. Techniques	Information Theory Coding & Cryptography
	EE	Power System Analysis	Power Apparatus & Systems
	Mechatronics	Mechatronics System Design	Instrumentation & Control
	CSE	Analysis & Design of Algorithm	Advanced Computer Architecture
27.11.15	ECE	Telecommn. Switching & Networks	Satellite Communication System
	EE	Advanced Power Eletronics	Power Quality
	Mechatronics	Applied Electronics & Microprocessors	Sensor & Actuators
	CSE	Object Oriented System	Real Time System
28.11.15	ECE	Analogue Integrated Circuit Design	
	EE	Extra High Voltage AC Transmission	
	Mechatronics	Digital Signal Processing & Applications	
	CSE	Wireless Sensor Network	

Copy to the:

1. Chairman's Office/Principal's Office/Dean PG&CD/Dean Academic/All concerned HOD's/ DSW/ DSW Office/Notice Boards.
2. SSEPL with a request to display it in college website immediately.


19/11/2015
Director


19/11/2015
Director